

REMARKS

1. In response to the Office Action mailed March 24, 2006, Applicant respectfully requests reconsideration. Claims 1-75 were originally presented for examination. Claims 18-75 were previously withdrawn. Claims 1-17 were rejected in the outstanding Office Action. Claim 9 has been canceled. No claims have been added. Claims 1-5 and 10-11 have been amended. Thus, upon entry of this paper, claims 1-8 and 10-75 will be pending in this application. Of these seventy-four (74) claims, seven (7) claims (claim 1, 18, 35, 43, 52, 54, and 55) are independent. Based on the above Amendments and following Remarks, Applicant respectfully requests that the outstanding objections and rejections be reconsidered, and that they be withdrawn.

Art of Record

2. Applicant acknowledges receipt of form PTO-892 identifying additional references made of record by the Examiner.

Election

3. During a telephone conversation between the Examiner and Michael Verga on March 14, 2006, a provisional election was made with traverse to prosecute the invention identified by the Examiner as Species 1. Applicant confirms the election of Species I, with traverse, as requested by the Examiner in the current Office Action. Applicant further notes that Examiner identified claim 1 as also generic to species II and III. Additionally, Applicant would like to note that FIG. 4B is an illustration of an exemplary embodiment in which an FBB module communicates with a controller and a hardware monitor 20, such as illustrated in FIGs. 1A, 2 and 3. As such, Applicant respectfully submits that claim 1 is also generic to species VII identified by the Examiner and claimed by the Examiner to be best illustrated by FIG. 4B.

4. Additionally, Applicant respectfully disagrees with the Examiner's identification of only claims 1-17 as drawn to FIG. 1A. Rather, Applicant respectfully submits that all currently pending claims may be practiced using the embodiment illustrated in the simplified diagram of FIG. 1A. For example, FIG. 1A illustrates a simplified diagram of a

computer system 10 and FIG. 2 illustrates a more specific embodiment of this computer system 10.

By way of example, FIGURE 2 schematically illustrates one embodiment of a margin testing of the invention incorporated in the computer system 10 that includes, in addition to the controller 14, a voltage control block/module (VCB) 34, a frequency control block/module (FCB) 36, and a Fault bypass block (FBB) 38. ... Although this exemplary margin testing system includes both a frequency and a voltage control block, other embodiments may include only a voltage control module or a frequency control module. (See, Applicants' specification at page 10.)

5. Likewise, FIG. 3, 4B, and 10 provide more specific illustrations of particular embodiments of the embodiment of FIG. 1A. Applicant further respectfully submits that the embodiment illustrated by the simplified diagram of FIG. 1A and specific implementations of same (e.g., described with reference to FIGs. 1A and 9A) may be used in practicing the flow charts illustrated in FIGs. 1B and 9C. As such, Applicant respectfully disagrees with the Examiner's assertion that only claims 1-17 are drawn to the embodiment illustrated by FIG. 1A. Rather, as discussed above, all currently pending claims may be practiced using the embodiment illustrated by the generic FIG. 1A and specific implementations of same. Therefore, Applicants request that for at least the above-discussed reasons, the Examiner reconsider the restriction requirement and the designation of only claims 1-17 as being drawn to the embodiment of FIG. 1A.

Claim Objections

6. In the Office Action, the Examiner objected to claims 4 and 14 for informalities. Applicant has amended claim 4 and the amendments to claim 1 obviate the objection to dependent claim 14. Applicant accordingly therefore requests that the Examiner reconsider and withdraw the objections.

Objections to Specification

7. The Examiner objected to the disclosure for informalities. Applicant has amended the disclosure on page 1 line 6 and accordingly respectfully request that the Examiner

reconsider and withdraw the objection to the specification.

Claim Rejections

8. Claims 1-8 and 13-16 have been rejected under 35 U.S.C §102(b) as being anticipated by U.S. Patent No. 6,617,872 to Vogley (hereinafter, referred to as “Vogley”). Claims 9-12 and 17 have been rejected under 35 U.S.C. §103(a) as being unpatentable over Vogley in view of U.S. Publication NO 2003/0120969 to Hawkins (hereinafter “Hawkins.”) Based on the above Amendments and following Remarks, Applicant respectfully requests that the outstanding objections and rejections be reconsidered, and that they be withdrawn.

9. Claim 1, as amended, recites, in part, “a baseboard management controller (BMC); and a digital parameter adjuster configured to communicate with said controller and set at least one operating parameter associated with at least one of said components to one or more test values in response to commands from said controller; and wherein said baseboard management controller is configured to monitor a response of said electronic system to said test values.” In rejecting claim 1, the Examiner relied on Vogley as allegedly disclosing a controller and digital parameter as recited by original claim 1. For the below discussed reasons, Applicant respectfully submits that Vogley fails to teach a BMC and parameter adjuster as recited by amended independent claim 1.

10. Vogley is directed to a integrated circuit test arrangement for testing an integrated circuit device at a variety of high clock rates. (*See*, Vogley at Abstract.) In the system of Vogley, individual integrated circuits (24 and 25) are plugged into test sockets (30 and 31). (*See*, Vogley at col. 2 lns. 57-60.) When an integrated circuit device, is to be tested a program is loaded into a microprocessor (44 and 45) associated with the integrated circuit device to be tested. (*See*, Vogley at col. 3 lns. 35-38.) The integrated circuit device is then tested and test results are stored by the microprocessor (44 and 45). (*See*, Vogley at col. 5 lns. 13-15.) As such, Vogley only discloses the microprocessor obtaining results for the individual integrated circuit device the microprocessor is associated with. Vogley accordingly does not teach or suggest that this microprocessor monitors a response of the electronic system to the test values.

11. Applicant therefore respectfully submits that amended claim 1 is in condition for allowance for at least the reason the Vogley fails to teach or suggest [a] “baseboard management controller [that] is configured to monitor a response of said electronic system to said test values,” as recited by claim 1. Applicant therefore respectfully requests that the Examiner reconsider and withdraw the rejection to claim 1 for at least this reason.

12. Applicant also respectfully submits that Hawkins, a secondary reference relied on by the Examiner in rejecting dependent claims 9-12 and 17 does not cure this defect. Although, Hawkins broadly discloses a BMC, it does not disclose margin testing nor monitoring the results of a system to margin testing.

13. Applicant accordingly respectfully submits that claim 1 is in condition for allowance for at least the reason that the cited references, whether taken alone or in combination, teach or suggest [a] “baseboard management controller [that] is configured to monitor a response of said electronic system to said test values,” as recited by claim 1. Applicant therefore respectfully requests that the Examiner reconsider and withdraw the rejection to claim 1 for at least this reason.

Motivation to Combine The Examiner has Failed to Provide Evidence of a Motivation to Combine

14. The §103 rejections are *prima facie* improper because they provide no appropriate basis for combining Vogley and Hawkins. As set forth in the Manual of Patent Examining Procedure (MPEP) at § 706.02(j), “To establish a *prima facie* case of obviousness . . . there must be some suggestion or motivation, *either in the references themselves or in the knowledge generally available to one of ordinary skill in the art*, to modify the reference or to combine reference teachings (emphasis added) . . . The teaching or suggestion to make the claimed combination and the reasonable expectation of success *must both be found in the prior art and not based on applicant’s disclosure* (emphasis added). *In re Vaeck*, 947 F.2d. 488, 20 USPQ2d 1438 (Fed. Cir. 1991).”

15. The Office Action attempts to justify combining Vogley with Hawkins based on the following conclusory statement:

It would have been obvious at the time the invention was made to one of ordinary skill in the art to modify Vogley to include the teachings of Hawkins because including a Baseboard Management Controller (BMC), implementing an Intelligent Platform Management Interface (IPMI) protocol., including an I²C-based bus, wherein said I²C-based bus is an IPMB, bus, and that said computer server would have allowed the skilled artisan to provide a star intelligent platform management bus topology. (*See*, Office Action at pg. 8.)

16. The above quoted conclusory statement fails to properly identify any text in Vogley, Hawkins, or any other reference that would provide any motivation for combining Vogley with Hawkins. Rather, the Examiner merely identifies particular elements (e.g., a BMC) disclosed by Hawkins, and then makes the conclusory statement that it would be obvious to combine these elements with Vogley. The Examiner, however, has identified nothing whatsoever that shows why someone would be motivated to combine the elements of Hawkins with Vogley. For example, the Examiner identified no teaching in Hawkins, or any other reference, explaining why someone would be motivated to use the BMC of Hawkins in place of the microprocessors of Vogley. Rather, the Examiner has used in permissible hindsight to pick and choose elements from Hawkins and then made the unsupported conclusory allegation that it would be obvious to combine these elements with Vogley.

17. As held by the Federal Circuit in *In re Lee*, 61 USPQ2d 1430, 1433 (Fed. Cir. 2002), specific reasons must be shown in the art suggesting a combination of references. (See also *In re Kotzab*, 217 F.3d 1365, 1371, 55 USPQ2d 1313, 1317 (Fed. Cir. 2000) (“[P]articular findings must be made as to the reason the skilled artisan, with no knowledge of the claimed invention, would have selected these components for combination in the manner claimed.”); *Also see, In re Rouffet*, 149 F.3d 1350, 1359, 47 USPQ2d 1453, 1459 (Fed. Cir. 1998) (“[E]ven when the level of skill in the art is high, the Board must identify specifically the principle, known to one of ordinary skill that suggests the claimed combination.”)).

18. As such, because the Examiner has failed to provide evidence of a motivation to combine Vogley and Hawkins, Applicant respectfully requests that the Examiner reconsider and withdraw the rejections under §103 for at least this additional reason.

19. Further, if the Examiner is relying on his own personal knowledge to supply this missing motivation to combine the references, Applicant requests that the Examiner provide an affidavit as required under pursuant to 37 C.F.R. §1.104(d)(2) (“when a rejection in an application is based on facts within the personal knowledge of an employee of the Office, the data shall be as specific as possible, and the reference must be supported, when called for by the applicant, by the affidavit of such employee”).

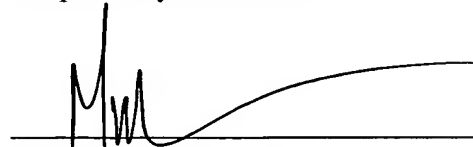
Dependent Claims

20. The dependent claims incorporate all of the subject matter of their respective independent claims and add additional subject matter which makes them *a fortiori* independently patentable over the art of record. Accordingly, Applicant respectfully requests that the outstanding rejections of the dependent claims be reconsidered and withdrawn.

Conclusion

21. In view of the foregoing, this application should be in condition for allowance. A notice to this effect is respectfully requested.

Respectfully submitted,

A handwritten signature in black ink, appearing to read 'Michael G. Verga', is written over a horizontal line.

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